



AMENDED CLAIMS					
	NO. OF CLAIMS	HIGHEST NO. OF CLAIMS PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	ADDT'L FEE
Total Claims	26	MINUS 20 =	6	x \$22 =	132.00
Independent Claims	3	MINUS 3 =	0	x \$78 =	0
If Amendment adds multiple dependent claims, add \$250.00					---
Total Amendment Fee					132.00
If small entity status is claimed, subtract 50% of Total Amendment Fee					---
<b>TOTAL ADDITIONAL FEE DUE FOR THIS AMENDMENT</b>					<b>\$132.00</b>

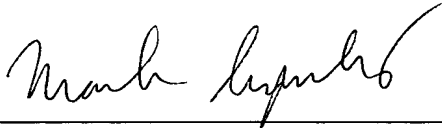
☒ A claim fee in the amount of \$ 132.00 is enclosed.

☐ Charge \$ \_\_\_\_\_ to Deposit Account No. 02-4800.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in triplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By:   
Mark A. Superko  
Registration No. 34,027

P.O. Box 1404  
Alexandria, Virginia 22313-1404  
(703) 836-6620

Date: March 18, 1996



132-103-2305

Patent  
Attorney's Docket No. 018414-148

*[Handwritten signatures and initials]*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
William K. ZURAVLEFF et al. ) Group Art Unit: 2305  
Application No.: 08/480,739 ) Examiner: Unassigned  
Filed: June 7, 1995 )  
For: NON-BLOCKING LOAD BUFFER )  
AND A MULTIPLE-PRIORITY )  
MEMORY SYSTEM FOR REAL- )  
TIME MULTIPROCESSING )

RECEIVED  
MAR 28 1996  
GROUP 2300

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE CLAIMS

Please add the following new claims:

~~21~~ 21. A multiple priority non-blocking load buffer according to Claim 7, wherein a maximum number of outstanding memory or I/O transactions is specified for said unique priority level in each of said sub-queues which prevents entries of memory or I/O requests having low priority levels from using one of said sub-queues before entries of memory or I/O requests having higher priority levels.

22. A multiple priority non-blocking load buffer according to Claim 1, wherein priorities corresponding to the entries of memory or I/O requests are determined by logical memory addresses, control bits derived from a memory management page table, control bits derived from segmentation entries, virtual addresses of a memory